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APPLICANTS

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** CONTINUING DATA *N* *VS* *****

** FOREIGN APPLICATIONS *N* *VS* *****

IF REQUIRED, FOREIGN FILING LICENSE
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 ** 11/18/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY CA	SHEETS DRAWING 9	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 2
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Verified and Acknowledged *Vishu S. Pillai* Examiner's Signature *VS* Initials

ADDRESS

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TITLE

Method of clock driven cell placement and clock tree synthesis for integrated circuit design

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